REMARKS

Claims 1-19 remain pending in the application. Claims 1, 2, 4, 6-9, 11-13 and 17-19 amended. Reconsideration of the rejection and allowance of the pending application in view of the following remarks are respectfully requested.

In the Final Office Action, the Examiner rejected claims 1-19 under 35 U.S.C. §102(b) as being anticipated by Nakayama et al. (International Patent Publication No. WO 2003/087856).

Applicants' claim 1, as currently amended, recites a network analyzer which includes, inter alia, a correction coefficient outputter that outputs measured first coefficients and second coefficients of a correction frequency converting element. A signal output from a first terminal is represented as a sum of a product of a signal input to the first terminal and a first coefficient and a product of a signal input to a second terminal and a second coefficient, a ratio of the magnitudes of the second coefficients is constant, and a frequency of the signal input to the first terminal is different from a frequency of the signal input to the second terminal.

Applicants' claim 7, as currently amended, recites a network analyzing method which includes, inter alia, outputting measured first coefficients and second coefficients of a correction frequency converting element. A signal output from a first terminal is represented as a sum of a product of a signal input to the first terminal and the first coefficient and a product of a signal input to a second terminal and the second coefficient, a ratio of the magnitudes of the second coefficients is constant, and a frequency of the signal input to the first terminal is different from a frequency of the signal input to the second terminal.

Applicants' claim 8, as currently amended, recites a program of instructions for execution by a computer to perform processing for analyzing a network. The processing includes, inter

alia, outputting measured first coefficients and second coefficients of a correction frequency converting element. A signal output from a first terminal is represented as a sum of a product of a signal input to the first terminal and the first coefficient and a product of a signal input to a second terminal and the second coefficient, a ratio of the magnitudes of the second coefficients is constant, and a frequency of the signal input to the first terminal is different from a frequency of the signal input to the second terminal.

Applicants' claim 9, as currently amended, recites a computer-readable medium having a program of instructions for execution by a computer to perform processing for analyzing a network. The processing includes, inter alia, outputting measured first coefficients and second coefficients of a correction frequency converting element. A signal output from a first terminal is represented as a sum of a product of a signal input to the first terminal and the first coefficient and a product of a signal input to a second terminal and the second coefficient, a ratio of the magnitudes of the second coefficients is constant, and a frequency of the signal input to the first terminal is different from a frequency of the signal input to the second terminal.

Nakayama et al. discloses a network analyzer 1 which measures S parameters of a Device Under Test (DUT) 2. The DUT 2 has an input terminal 2a and an output terminal 2b, a signal frequency of a signal at the input terminal 2a is f1, and a signal frequency of a signal at the output terminal 2b is f2. See, e.g., page 31, lines 24-30 of the translation of Nakayama et al.

On page 6 of the Final Office Action, the Examiner asserts that if the frequency f1 is equal to the frequency f2, a ratio of the magnitudes of the S parameters will be constant. However, Applicants' claim 1, as currently amended, recites that a frequency of the signal input to the first terminal is <u>different from</u> a frequency of the signal input to the second terminal.

Applicants respectfully submit that Nakayama et al. does not disclose second coefficients having a ratio of magnitudes which are constant when a frequency of a signal input to a first terminal is different from a frequency of a signal input to a second terminal.

Accordingly, Applicants respectfully submit that Nakayama et al. fails to disclose or suggest a network analyzer which includes a correction coefficient outputter that outputs measured first coefficients and second coefficients of a correction frequency converting element, where a signal output from a first terminal is represented as a sum of a product of a signal input to the first terminal and a first coefficient and a product of a signal input to a second terminal and a second coefficient, a ratio of the magnitudes of the second coefficients is constant, and a frequency of the signal input to the first terminal is different from a frequency of the signal input to the second terminal, as recited in Applicants' amended claim 1.

For at least these reasons, Applicants submit that the invention recited in Applicants' claim 1 is not anticipated by Nakayama et al, and request that the Examiner withdraw the rejection under 35 U.S.C. §102(b) and allow claim 1. Applicants submit that independent claims 7-9, which are directed towards a related method, program and computer-readable medium, are also in condition for similar reasons.

Applicants submit that claims 2-6 and 10-19 are also in condition for allowance, in view of their dependency from claim 1.

Based on the above, it is respectfully submitted that this application is now in condition for allowance, and a Notice of Allowance is respectfully requested.

SUMMARY AND CONCLUSION

Entry and consideration of the present amendment, reconsideration of the outstanding Final Office Action, and allowance of the present application and all of the claims therein are respectfully requested and now believed to be appropriate. Applicants have made a sincere effort to place the present invention in condition for allowance and believe that they have done so.

Any amendments to the claims which have been made in this amendment, and which have not been specifically noted to overcome a rejection based upon the prior art, should be considered to have been made for a purpose unrelated to patentability, and no estoppel should be deemed to attach thereto.

Should an extension of time be necessary to maintain the pendency of this application, including any extensions of time required to place the application in condition for allowance by an Examiner's Amendment, the Commissioner is hereby authorized to charge any additional fee to Deposit Account No. 19-0089.

Should the Examiner have any questions or comments regarding this response, or the present application, the Examiner is invited to contact the undersigned at the below-listed telephone number.

Respectfully submitted,
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